

MOS FIELD EFFECT TRANSISTOR μ PA1853

P-CHANNEL MOS FIELD EFFECT TRANSISTOR **FOR SWITCHING**

DESCRIPTION

The μ PA1853 is a switching device which can be driven directly by a 4-V power source.

The μ PA1853 features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

FEATURES

- Can be driven by a 4-V power source
- · Low on-state resistance

 $R_{DS(on)1} = 85 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = -10 \text{ V, ID} = -1.5 \text{ A)}$

- $R_{DS(on)2} = 152 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = -4.5 \text{ V, Ip} = -1.5 \text{ A})$
- $R_{DS(on)3} = 180 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = -4.0 \text{ V, Ip} = -1.5 \text{ A)}$

ORDERING INFORMATION

PART NUMBER	PACKAGE
μPA1853GR-9JG	Power TSSOP8

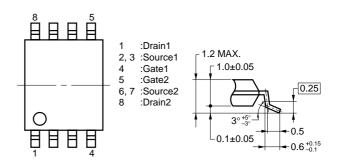
ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

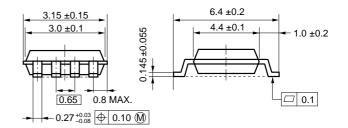
Drain to Source Voltage	Voss	-30	V
Gate to Source Voltage	Vgss	-20/+5	V
Drain Current (DC)	I _{D(DC)}	∓ 2.5	Α
Drain Current (pulse) Note1	D(pulse)	∓ 10	Α
Total Power Dissipation Note2	Рт	2.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1 %

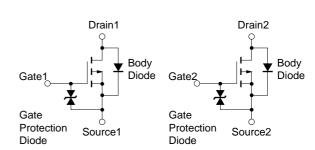
2. Mounted on ceramic substrate of 5000 mm² x 1.1 mm

PACKAGE DRAWING (Unit: mm)





EQUIVALENT CIRCUIT



The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

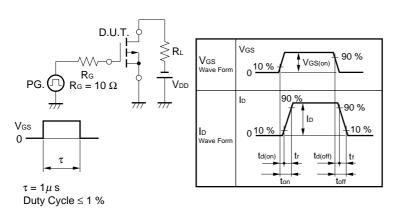
Remark



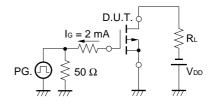
★ ELECTRICAL CHARACTERISTICS (TA = 25 °C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Cut-off Current	IDSS	V _{DS} = -30 V, V _{GS} = 0 V			-10	μΑ
Gate Leakage Current	Igss	$Vgs = \mp 20 \text{ V}, Vds = 0 \text{ V}$			∓ 10	μΑ
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = -10 V, I _D = -1 mA	-1.0	-1.7	-2.5	٧
Forward Transfer Admittance	yfs	V _{DS} = -10 V, I _D = -1.5 A	1	3.6		S
Drain to Source On-state Resistance	RDS(on)1	Vgs = -10 V, ID = -1.5 A		64	85	mΩ
	RDS(on)2	Vgs = -4.5 V, ID = -1.5 A		114	152	mΩ
	RDS(on)3	Vgs = -4.0 V, ID = -1.5 A		135	180	mΩ
Input Capacitance	Ciss	V _{DS} = −10 V		520		pF
Output Capacitance	Coss	V _G S = 0 V		200		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		82		pF
Turn-on Delay Time	t d(on)	V _{DD} = −10 V		60		ns
Rise Time	tr	ID = -1.5 A		220		ns
Turn-off Delay Time	t _{d(off)}	$V_{GS(on)} = -10 \text{ V}$		800		ns
Fall Time	t _f	$R_G = 10 \Omega$		620		ns
Total Gate Charge	Q _G	V _{DD} = -24 V		12		nC
Gate to Source Charge	Qgs	I _D = −2.5 A		2		nC
Gate to Drain Charge	QGD	Vgs = −10 V		3		nC
Diode Forward Voltage	V _{F(S-D)}	IF = 2.5 A, VGS = 0 V		0.73		V

TEST CIRCUIT 1 SWITCHING TIME

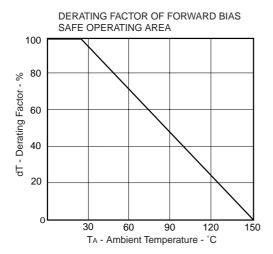


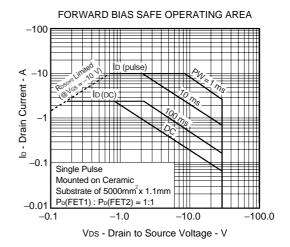
TEST CIRCUIT 2 GATE CHARGE

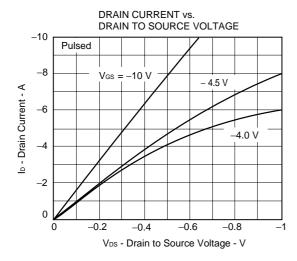


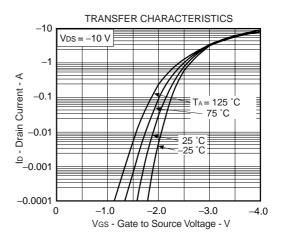


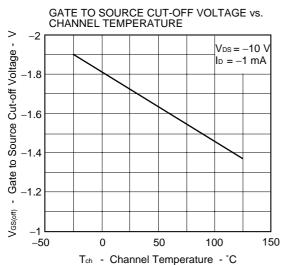
★ TYPICAL CHARACTERISTICS (TA = 25°C)

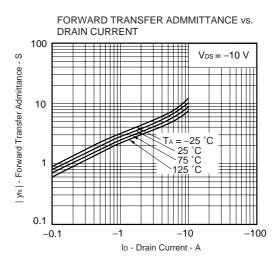




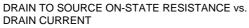


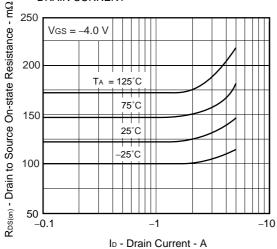




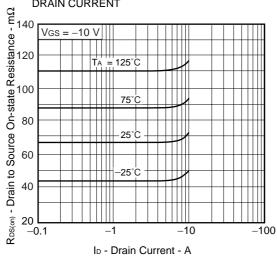


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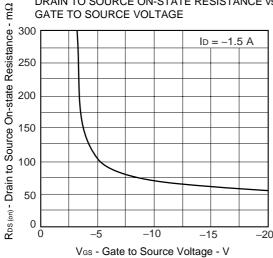




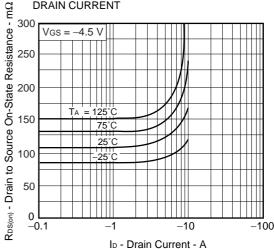
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



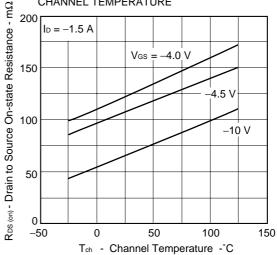
DRAIN TO SOURCE ON-STATE RESISTANCE vs.

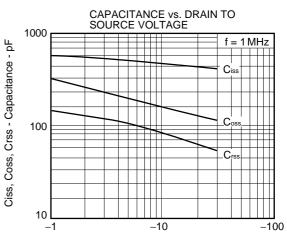


DRAIN TO SOURCE ON-STATE RESISTANCE vs. **DRAIN CURRENT**



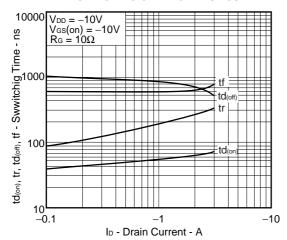
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



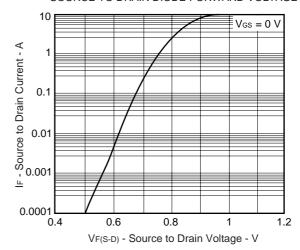


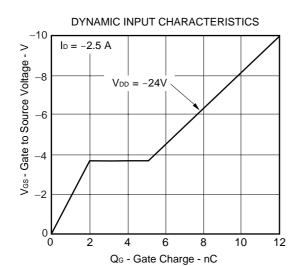
V_{DS} - Drain to Source Voltage - V

SWITCHING CHARACTERISTICS

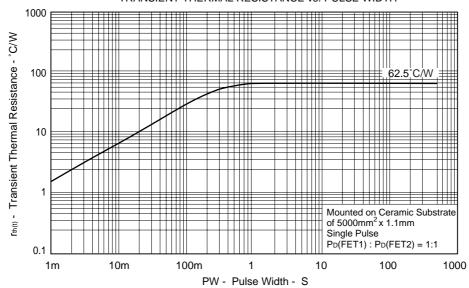


SOURCE TO DRAIN DIODE FORWARD VOLTAGE





TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



NEC μ PA1853

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